



Abstract

A signal processor for a joystick which includes a joystick input device (11) which varies a joystick voltage input value V_i according to an operating amount of a joystick (10) from a neutral position (11), an input circuit (13) which outputs the average value of the joystick voltage input value V_i read at every sampling time over a predetermined number of past occasions as a joystick voltage computation value V_{ic} , a computation circuit (14) which computes an output computation value V_{oc} set according to the joystick voltage computation value V_{ic} , and operation start detecting circuit which detects an operation start when the joystick (10) is pushed over from the neutral position. The computation circuit (14) increases the output computation value V_{oc} to an effective maximum value when operation starts. In this way, over-sensitivity of the control response when there is a sudden operation of the joystick (10) is suppressed.